

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Noriaki HIRAGA)	
)	
Application No.: Unassigned)	Group Art Unit: Unassigned
)	
Filed: August 18, 2003)	Examiner: Unassigned
)	
For: SEMICONDUCTOR INTEGRATED)	
CIRCUIT DEVICE WITH ENHANCED)	
RESISTANCE TO ELECTROSTATIC)	
BREAKDOWN)	

Commissioner for Patents
U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window, **MS Patent Application**
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(b)**

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b). Applicant brings to the attention of the Examiner the documents listed on the attached PTO-1449. This Information Disclosure Statement is being filed before the mailing of a first Office Action on the merits. Thus, Applicant respectfully submits that no fee is due for the submission of this paper.

In accordance with 37 C.F.R. § 1.98(d)(1), copies of each document listed on the PTO Form 1449 are not attached because they were previously provided in U.S. Application No. 09/625,643, from which the present Application claims priority under 35 U.S.C. § 120. Applicant respectfully requests that the Examiner consider the listed documents and evidence that consideration by making appropriate notations on the attached PTO Form 1449.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents is material or constitute "Prior Art." If it should be determined that any of the listed documents does not constitute "Prior Art" under United States law, Applicant reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such document.

Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the document be applied against the claims of the present application.

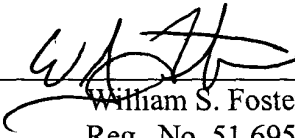
EXCEPT for issue fees payable under 37 C.F.R. § 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §§ 1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account 50-0310. This paragraph is intended to be a **CONSTRUCTIVE PETITION FOR EXTENSION OF TIME** in accordance with 37 C.F.R. § 1.136(a)(3).

Respectfully submitted,

MORGAN, LEWIS & BOCKIUS LLP

Date: August 18, 2003

By:



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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) PTO Form 1449				Attorney Docket No. 052593-5003-01		Application No.: Unassigned	
				Applicant(s): Noriaki HIRAGA PAGE 1 of 1			
				Filing Date:		Group Art Unit: Unassigned	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Sub Class	Filing Date
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub Class	<u>Translation</u> YES NO
		JP 08-037238	06/02/1996	Japan			
		JP 07-022617	24/01/1995	Japan			
		JP 02-192760	30/07/1990	Japan			
		JP 11-505374	18/05/1999	Japan			See USSN 08/439,929
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
Examiner				Date Considered			
Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							